SYSTEM

- Up to 8-Site Parallel Testing
- Cable Mount
- Text Mode Test Program Develop Environments
- Tester Controller: PC with Windows XP
- AC Power: 220V@30A±5%, Single phase, 3-wire (L, N, G configured)

ARBITRARY WAVEFORM & DIGITIZER BOARD

- Pin Counts Per Module: 2
- 4 Single-ended or 2 Differential
- Resolution: 16 Bits
- Sampling Rate: 40M SPS Max, 625k SPS min
- Waveform Memory: 4M
- Output Range: ±5Vpp@50 ohm Load ±10Vpp@a High-Impedance Load
- Accuracy: ±0.1dB
- DC Offset Range: ±2.5V@50 ohm Load
- Accuracy: ±5m\
- · Sine Waveform:
- Harmonic products ans spurs: -75dBc
- Output Impedance: 50 ohm: 75 ohm.
- Filter: Analog, 2MHz, 500kHz, 50kHz, 5k, Through. Trigger Input:
- TTL Rising Edge PW: 40ns min
- SYNC Output: TTL Duty Cycle: 20% to 80%
- Marker Output: TTL 8 Sample Clock Periods
- Frequency Range: 1Hz 1MHz

Digitizer

- Pin Counts Per Module: 4
- Resolution: 500KSPS 16-1bit, 10MSPS 14-bit
- · Sampling Rate: 10M SPS max
- Waveform Memory: 4M
- · Input Mode: Single-ended or Differentials
- Input Range: ±10V(500K), ±5V, ±2.5V, ±1V, ±0.5V, ±0.1V
- DC Offset Range: ±2V
- Impedance: 1M ohm, 50 ohm for Single-ended 10K ohm, 100 ohm for Differentials
- Input Coupling Mode: DC, AC Coupling
- Filter: 2KHz, 10KHz, 2MHz, 1KHZ Notch Filter
- Trigger Mode:
- TTL Rising Edge PW: 40ns min
- Master/Slave Form Arbitrary Waveform Module

DVC PRECISION MEASUREMENT BOARD (DUAL CHANNEL)

- Two Independent, Full Four-Quadrant V/I Source
- Voltage to ±45V, Current to ±2A
- · Voltage Force and Measure Range: 1V, 2V, 4V, 8V, 16V, 32V, 48V
- Voltage Force and Measure Resolution: 16 bits
- Voltage Force Accuracy:
- ±(0.05% of value+0.05% of range)

- Voltage Measure Accuracy: ±(0.03% of+0.03% of range)
- Voltage Clamp Resolution: 16 bits
- Current Force and Measure Range: 2μA, 20μA, 200μA, 2mA, 200mA, 1A, 2A
- · Current Force and Measure Resolution: 16 bits
- Current Force and Measure Accuracy: 2µA - 200µA:
- ±(0.05% of value+0.05% of range+10nA)
- 2mA 200mA:
- ±(0.1% of value+0.1% of range) 1A - 2A:
- $\pm (0.3\% \text{ of value} + 0.1\% \text{ of range})$
- Current Clamp Resolution: 16 bits
- Measure Resolution: 16 bit
- Measure Accuracy: ±(0.2% of range+200uV) 20mV, 100mV, 200mV, 1V, 2V, 4V, 10V, 20, 100V

OVC PRECISION MEASUREMENT BOARD (OCTAL CHANNELS)

Differential Voltage Measure between Two Channels

- 8 independent V/I Resource
- System Ground
- Voltage Force and Measure Range: 2V, 4V, 8V, 16V, 20V
- Voltage Force and Measure Resolution: 16 bits
- · Voltage Force and Measure Accuracy: 2V: ±(0.05% of range)
- 4V: ±(0.05% of range)
- 8V: ±(0.05% of range)
- 16V: ±(0.05% of range) 20V: ±(0.05% of range)
- Voltage clamp Resolution: 16 bits
- Current Force and Measure Range: 1uA, 10uA, 100uA, 1mA, 10mA, 100mA, 200mA
- · Current Force and Measure Resolution: 16 bits
- · Current Force and Measure Accuracy: 1µA:±(0.2%+2nA)
- 10µA: ±(0.1%+100nA)
- 100µA: ±(0.1%+100nA)
- 1mA: ±(0.1%+1µA)
- 10mA: ±(0.1%+10µA) 100mA: ±(0.1%+100µA)
- 200mA: ±(0.1%+200µA)
- Current Clamp Resolution: 16 bits
- 10KSPS DDS programmable to any Channel
- 10KSPS Digitizer Multiplexed to Two Channels
- DVM Differential Voltage Measure between Two Channels

QVC BOARD (QUAD VOLTAGE CURRENT)

- 4 independent V/I resource
- 16 bit ADC for voltage and current measure
- Vmax: ±45V
- Imax:±1A
- Vrange: 1V, 2V, 4V, 8V, 16V, 20V, 32V, 45V Accuracy: ±(0.05% of value+0.05% range)

- Irange: 2μA, 20μA, 200μA:
- Accuracy: ±(0.1% of value+0.1% of range+10nA) 2mA, 20mA, 200mA, 1A:

MVC BOARD

- 2 independent V/I resource
- 16 bit ADC for voltage and current measure

Accuracy: ±(0.1% of value+0.1% of range)

- Vmax: +100V to -45V
- Imax:+500mA
- Vrange: 4V, 8V, 16V, 32V, 50V, 100V
- Accuracy: ±(0.05% of value+0.1% of range) Irange: 2uA, 20uA, 200uA:
- Accuracy: ±(0.1% of value+0.2% of range+10nA) 2mA, 20mA, 200mA, 500mA:

Accuracy: ±(0.1% of value+0.2% of range)

NAC BOARD (NANO AMPERE CURRENT)

- 2 Channels Floating Ground System
- 16 bit ADC for voltage and current measure
- Vmax: ±40V
- Imax:+100mA
- Vrange: 2.5V.5V.10V.20V.40V
- Accuracy: ±(0.05% of value+0.1% of range) • Irange: 10nA, 100nA, 1μA, 10μA, 100μA, 1mA,
- 10mA, 100mA Accuracy: ±(0.1% of value+0.1% of range)

MFB BOARD (MULTIPLE FUNCTION BOARD)

- 8 Pairs Channels (AWG/WD)
- RAMP force accuracy: ±4V Range: 1mV
- Measure accuracy: 4V: 1mV
 - 10V: 2.5mV
- AWG/RAMP sample rate: 1Msps@64K length

±12V Range: 2.5mV

- Digitizer Sample rate: 250Ksps@64K length
- · SDIO (Data Acquisition) Sample Rate: 1Msps@64K length

PVC PULSED V/I RESOURCE

- 2 Independent V/I Resource
- Floating Ground, Pulse Mode Voltage Force and Measure Range
- 4V. 8V. 16V. 32V. 48V
- Voltage Force and Measure Resolution: 16 bits
- Irange: 150mA, 300mA, 1.5A, 3A, 5A, 10A, 15A, 30A
- Vmax: ±45V
- Imax:30A

Voltage Force and Measure Accuracy:

- 8V:±(0.3%+12mV)
- 16V: ±(0.3%+24mV)
- 32V: ±(0.3%+48mV)
- 46V: ±(0.3%+96mV)
- Voltage Clamp Resolution: 16 bits
- Current Force and Measure Resolution: 16 bits
- Current Force and Measure Accuracy:
- 150mA: ±(0.5%+0.375mA)
- 300mA: ± (0.5%+0.75mA)
- 1.5A: ±(0.5%+3.75mA)
- 3A:±(0.5%+7.5mA) 5A: ±(0.5%+12.5mA)
- 10A: ±(0.5%+25mA) 15A: ±(1%+37.5mA)
- 30A: ±(1%+75mA)
- Programmable Pulse Period Protection Timer

TMU & RELAY CONTROL BOARD

- CPU Trigger or External Trigger
- Positive/Negative Slope
- Counter, Time, Rising/Failing Time
- Time Resolution: 625ps Propagation Delay Timeout Function and Overflow State
- Input Waveform: Sine, Square, Sawtooth

Low Impedance Inputs: A. B

- ±10V Range: Impedance 2K/1M Ohm Nominal
- ±2.5V Range: Impedance 2K/1M Ohm Nominal Input Threshold Accuracy: 0.5% of Range
- High Impedane Inputs Impedance:
- Range: ±5V Zin > 4M Ohm
- Range: ±20V Zin = 2.1M Ohm Range: ±50V Zin = 2.1M Ohm

MATRIX BOARD

Two-wire Force/Sense Input Channel

Channel: 80 (Use DS2803 as Relay Driver)

- Two-wire Force/Sense Output Channel · Contact Current: 1A
- Contact Voltage: 60V Type Resistor: <1Ω

AUTO-CALIBRATION & DIAGNOSTIC BOARD

- Standard Cells
- · Standard DVM and GPIB Interface PEB64 PATTERN BOARD
- · Pin Configuration (I/O Channels): 64 pins
- Parametric Measurement Unit: 8 Sets

- · Voltage Force and Measure Accuracy: ±(1% of range)
- Current Force and Measure Accuracy: ±(1% of range)
- · Pin Level Source: Per Pin
- Driver Voltage Range: -2.0V to +8V Accuracy: ±(0.3% of 8V range)
- Comparator Voltage Range: -2.0V to +8V Accuracy: ±(0.5% of 8V range)
- Min. Pulse Width: 10ns
- Micro Instruction: CALL, RET, MATCH, MPAT, MLEN, RPT, LOOP, LPEND, STOP, TS
- Min. Period: 30ns 40ms
- Period Resolution: 10ns Data Rate: 33MHz
- Timing Generators: 4 edge/Pin
- Edge Placement Resolution: 2.5ns
- Timing Set: 16 change on the fly • Drive Mode: 0, 1, NF, NRZ, RZ, RO, SBC
- IO Mode: 0, 1, NF
- · Strobe Mode: Edge/Window
- Pattern Memory Depth: 4M 8M Fail Memory Depth: 4K

· Time Measurement Resolution: 10ns

OP-LOOP BOARD Four OP AMP Loop

Pattern Symbol: 0/1//X/Z/L/H

- DC Measurement: 16 bits
- Common Mode DAC: 16 bits AC Stimulus: 16 bits High Speed DAC AC measurement: 10MSPS, 12 Bit ADC
- · Loop Reference DAC: 16 bits
- Input Offset Voltage (Vos) Input Offset Current (los)
- Command Mode Rejection Ration (CMRR)
- Power Supply Rejection Ration (PSRR)
- Gain-Bandwidth Product (GBW) Slew Rate (SR)

KVSBOARD

Open Loop Gain (Avol)

- 2 Channels Floating Ground System
- 16bit ADC for voltage and current measure

Irange: 15μA, 150μA, 1.5mA, 15mA I accuracy: 0.35% of range

- 16 bit DAC for voltage and current force Vmax: 1000V
- Vrange: 100V, 200V, 500V, 1000V accuracy: 0.25% of range
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Time Max: 10 sec

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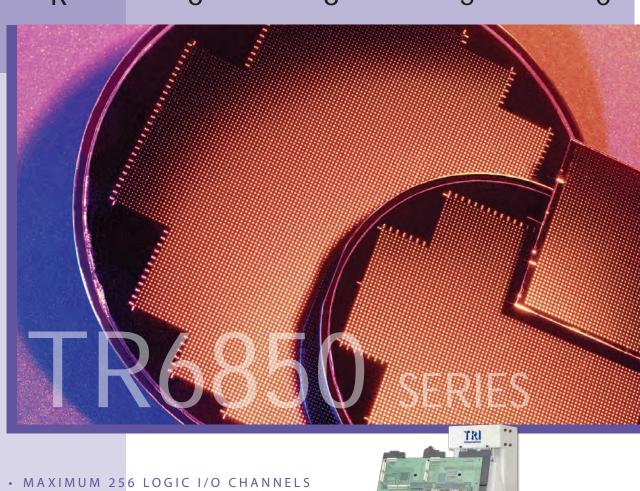
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- San Jose, CA 95131 U.S.A. 11900 Bayan Lepas Penang, Malaysia TFI: +604-6461171
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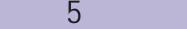


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TMU FOR TIMING PARAMETERS

MEASUREMENT

AND DIGITIZER

• MULTIPLE V/I MODULES ARE SUITABLE FOR WIDE RANGE OF TEST APPLICATION

 HIGH-PRECISION MEASUREMENT UNIT • ARBITRARY WAVEFORM GENERATOR

• EASY PROGRAMMING & DEBUGGING

RICH DEBUGGING & ANALYTIC TOOLS



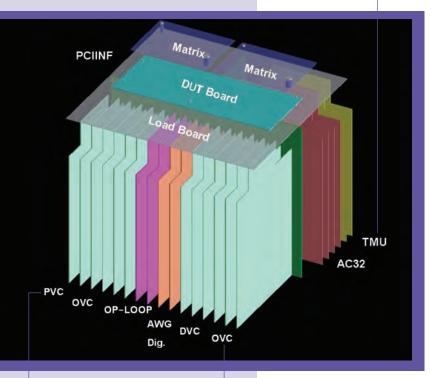
FEATURES TR6850

■ TMU:

 80 Relay-control and 4 TMUs Timing Parameters Testing

for Pulse Width, Pulse Period Frequency, Propagation Delay, and Rising/Falling Time

THE BEST "POWER MANAGEMENT IC+ MIXED SIGNAL" TESTER



The TR6850 features a text mode program that links with Microsoft Visual Studio. NET 2005 for rich debugging and analytic tools. It runs on a Windows XP operating system and offers a user-friendly GUI.

TR6850 seamlessly connects to all popular probers and handlers, and supports max. 8-site parallel testing capability.

All these features make TR6850 the best "Power Management IC+Mixed Signal" tester for both engineering verification and mass production.

TR6850 Hardware Architecture

OVC:

• 8 PMU with up to ±20V, ±200mA Forcing and Measuring Capability

16-bit resolution

● PVC:

- 2 Floating Ground Pulse PMU with up to ±45V and 30A Forcing and Measuring Capability
- 16-bit Resolution

FEATURES:

- 64 to 256 pin logic I/O pin with 64 pin increments.
- High-precision measurement unit.
- 40MSPS/16bit Arbitrary waveform generator, 10MSPS/14 bit Digitizer.
- Max. 8-Sites parallel testing.
- Enable grading the test results.
- Easy Programming & Debugging: Link with Visual Studio.NET to develop a C++ test program.
- Rich Debugging & Analytic Tools: Module Debug Tools, SPC, Parametric Wafer Map Tool, Pattern Editor, Shmoo, etc.
- Capability to generate Wafer Map Files & Trim Files.
- Multiple Store Modes to meet different requirements in saving Datalog.
- · Yield Alarm Management.
- Able to analyze test performance.

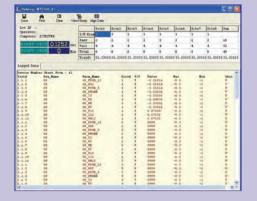
TEST PROGRAM EDITOR

The TR6850 links with Visual Studio.NET to provide an easy programming, easy debugging environment. TR6850 S/W system will auto-create a new function skeleton for each new inserted sequence item. Users only need to focus on developing test logic. Users can easily add 3rd party's library to enhnace test performance or control external instruments.

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DATALOG WINDOW

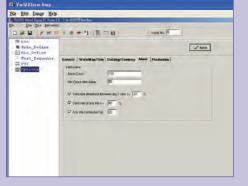
Display readable aligned test data, 1 line per test parameter. If test result is fail, the output will be highlighted in red.



YIELD ALARM

The TR6850 will issue a warning message & turn prober buzzer ON when yield is abnormal. The yield management can warn the operator to check what's wrong early and improve the mass production yield. There are 3 abnormal yield types that can be checked out:

- 1. Any two sites' yield difference is greater than a set criterion.
- 2. Any site's yield is too low.
- 3. Any site's continuous fail count exceeds a specified count.



PRODUCTION WINDOW

Compact window to display test results & summary in mass production.



TOOLS The timing diagram help user

understand relative timing among tester input and device output. With the graphic waveform, users can verify edited waveform as well.

AC RESULT ANALYSIS



Al Sino Fera, Same | Trisr jill, Ref Total Marker | 2000 -148957.80 0.157769 X Range -100001 80000 Unit USL LSL Max Min. Mean ShiDev Cp Cpt Ch Value 8.2 d 0.157769 0.000000 0.832580 0.024613 1.365428 0.329572 if sind 8

SPC TOOL

TEST SEQUENCE

Table to designate compared

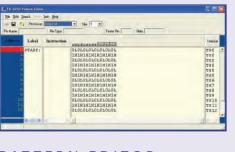
parametrics and test flow.

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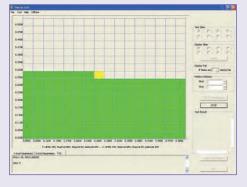
EDITOR

Engineering analysis tool to view each test parameter's statistics: test value histogram, min/max/mean value, standard deviation, Cp, Cpk.



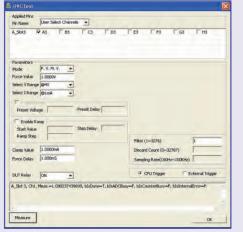
PATTERN EDITOR

View pattern file offline or view pattern memory online. Enable modifing pattern and save to file, also refreshing the patterns online by designating a specified range.



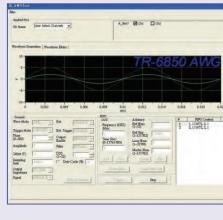
SHMOO TOOL

Enable to vary multiple parameters simultaneously to scan device's boundary.



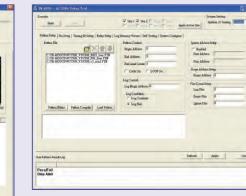
DC DEBUG TOOLS

The TR6850 provides several DC modules (eg. OVC, PVC, OPAMP-LOOP) to measure DC parameters in different purposes. Users can change the testing parameters to process various measurements in the tool.



AWG/DIGITIZER DEBUG TOOLS

Use AWG & Digitizer debug tools to control the TR6850 AWG/Digitizer module for debugging purpose.



AC DEBUG TOOLS

The TR6850 provides two AC modules (eg. PEB64, TMU) to measure AC parameters in different purposes. Users can change the testing parameters to process various measurements in the tool.

SYSTEM

- Up to 8-Site Parallel Testing
- Cable Mount
- Text Mode Test Program Develop Environments
- Tester Controller: PC with Windows XP
- AC Power: 220V@20A±5%, Single phase, 3-wire (L, N, G configured)

ARBITRARY WAVEFORM & DIGITIZER BOARD

- Pin Counts Per Module: 2
- 4 Single-ended or 2 Differential
- Resolution: 16 Bits
- Sampling Rate: 40M SPS Max, 625k SPS min
- Waveform Memory: 4M
- · Output Range: ±5Vpp@50 ohm Load
- Accuracy: ±0.1dB
- DC Offset Range: ±2.5V@50 ohm Load
- Accuracy: ±5m\
- Sine Waveform:
- Harmonic products ans spurs: -75dBc
- Output Impedance: 50 ohm: 75 ohm.

±10Vpp@a High-Impedance Load

- Filter: Analog, 2MHz, 500kHz, 50kHz, 5k, Through. Trigger Input:
- TTL Rising Edge PW: 40ns min
- SYNC Output: TTL Duty Cycle: 20% to 80%
- Marker Output: TTL 8 Sample Clock Periods
- Frequency Range: 1Hz 1MHz

Diaitizer

- Pin Counts Per Module: 4
- Resolution: 500KSPS 16-1bit, 10MSPS 14-bit
- Sampling Rate: 10M SPS max
- Waveform Memory: 4M Input Mode: Single-ended or Differentials
- Input Range: ±10V(500K), ±5V, ±2.5V, ±1V,
- ±0.5V, ±0.1V
- DC Offset Range: ±2V
- Impedance: 1M ohm, 50 ohm for Single-ended 10K ohm, 100 ohm for Differentials
- · Input Coupling Mode: DC, AC Coupling
- Filter: 2KHz, 10KHz, 2MHz, 1KHZ Notch Filter
- Trigger Mode
- TTL Rising Edge PW: 40ns min
- Master/Slave Form Arbitrary Waveform Module

DVC PRECISION MEASUREMENT BOARD (DUAL CHANNEL)

- Two Independent, Full Four-Quadrant V/I Source
- Voltage to ±45V, Current to ±2A · Voltage Force and Measure Range:
- 1V. 2V. 4V. 8V. 16V. 32V. 48V
- Voltage Force and Measure Resolution: 16 bits
- Voltage Force Accuracy:
- \pm (0.05% of value+0.05% of range)

- Voltage Measure Accuracy: ±(0.03% of+0.03% of range)
- Voltage Clamp Resolution: 16 bits
- Current Force and Measure Range: 2μA, 20μA, 200μA, 2mA, 200mA, 1A, 2A
- Current Force and Measure Resolution: 16 bits
- Current Force and Measure Accuracy: 2uA - 200uA:
- ±(0.05% of value+0.05% of range+10nA)
- 2mA 200mA:
- $\pm (0.1\% \text{ of value} + 0.1\% \text{ of range})$
- 1A 2A:
- ±(0.3% of value+0.1% of range) Current Clamp Resolution: 16 bits
- Differential Voltage Measure between Two Channels
- Measure Resolution: 16 bit
- Measure Accuracy: ±(0.2% of range+200uV) 20mV, 100mV, 200mV, 1V, 2V, 4V, 10V, 20, 100V

OVC PRECISION MEASUREMENT BOARD (OCTAL CHANNELS)

- 8 independent V/I Resource
- System Ground
- Voltage Force and Measure Range: 2V. 4V. 8V. 16V. 20V
- Voltage Force and Measure Resolution: 16 bits
- Voltage Force and Measure Accuracy:
- 2V: ±(0.05% of range) 4V: ±(0.05% of range)
- 8V: ±(0.05% of range)
- 16V: ±(0.05% of range)
- 20V: ±(0.05% of range) · Voltage clamp Resolution: 16 bits
- Current Force and Measure Range: 1μA, 10μA, 100μA, 1mA, 10mA, 100mA, 200mA
- Current Force and Measure Resolution: 16 bits
- Current Force and Measure Accuracy:
- 1uA: ±(0.2%+2nA)
- 10µA: ±(0.1%+100nA)
- 100uA: ±(0.1%+100nA) 1mA: ±(0.1%+1µA)
- 10mA: ±(0.1%+10µA)
- 100mA: ±(0.1%+100μA)
- 200mA: ±(0.1%+200µA)
- Current Clamp Resolution: 16 bits
- 10KSPS DDS programmable to any Channel
- 10KSPS Digitizer Multiplexed to Two Channels DVM Differential Voltage Measure between
- Two Channels

QVC BOARD (QUAD VOLTAGE CURRENT)

- 4 independent V/I resource
- · 16 bit ADC for voltage and current measure
- Vmax: ±45V
- Vrange: 1V, 2V, 4V, 8V, 16V, 20V, 32V, 45V

Accuracy: ±(0.05% of value+0.05% range)

- Irange: 2μA, 20μA, 200μA:
- Accuracy: ±(0.1% of value+0.1% of range+10nA) 2mA, 20mA, 200mA,1A: Accuracy: ±(0.1% of value+0.1% of range)

MVC BOARD

- 2 independent V/I resource
- 16 bit ADC for voltage and current measure
- Vmax: +100V to -45V
- Imax: ±500mA
- Vrange: 4V, 8V, 16V, 32V, 50V, 100V
- Accuracy: ±(0.05% of value+0.1% of range) • Irange: 2μA, 20μA, 200μA:
- Accuracy: ±(0.1% of value+0.2% of range+10nA) 2mA, 20mA, 200mA, 500mA;

Accuracy: ±(0.1% of value+0.2% of range)

NAC BOARD (NANO AMPERE CURRENT)

- · 2 Channels Floating Ground System
- 16 bit ADC for voltage and current measure
- Vmax: ±40V
- Imax: ±100mA
- Vrange: 2.5V, 5V, 10V, 20V, 40V Accuracy: ±(0.05% of value+0.1% of range)
- Irange: 10nA,100nA,1μA,10μA,100μA,1mA,

10mA,100mA Accuracy: ±(0.1% of value+0.1% of range)

MFB BOARD (MULTIPLE FUNCTION BOARD)

- 8 Pairs Channels (AWG/WD)
- RAMP force accuracy: ±4V Range: 1mV ±12V Range: 2.5mV
- Measure accuracy: 4V: 1mV
- 10V: 2.5mV AWG/RAMP sample rate:1Msps@64K length
- Digitizer Sample rate: 250Ksps@64K length
- · SDIO (Data Acquisition) Sample Rate: 1Msps@64K length

PVC PULSED V/I RESOURCE

- 2 Independent V/I Resource
- · Floating Ground, Pulse Mode
- Voltage Force and Measure Range: 4V. 8V. 16V. 32V. 48V
- Voltage Force and Measure Resolution: 16 bits
- Irange: 150mA, 300mA, 1.5A, 3A, 5A, 10A, 15A, 30A Vmax: +45V
- Imax: 30A

Voltage Force and Measure Accuracy:

- 8V:±(0.3%+12mV)
- 16V: ±(0.3%+24mV
- 32V: ±(0.3%+48mV)
- 46V: ±(0.3%+96mV)
- Voltage Clamp Resolution: 16 bits
- Current Force and Measure Resolution: 16 bits
- Current Force and Measure Accuracy:
- 150mA: ±(0.5%+0.375mA)
- 300mA: ±(0.5%±0.75mA) 1.5A: ±(0.5%+3.75mA)
- 3A: ±(0.5%+7.5mA)
- 5A: ±(0.5%+12.5mA)
- 10A: ±(0.5%+25mA)
- 15A: ±(1%+37.5mA) • 30A: ±(1%+75mA)
- Programmable Pulse Period Protection Timer

TMU & RELAY CONTROL BOARD

- CPU Trigger or External Trigger
- Positive/Negative Slope
- Counter, Time, Rising/Failing Time
- Time Resolution: 625ps Propagation Delay Timeout Function and Overflow State
- Input Waveform: Sine, Square, Sawtooth
- Low Impedance Inputs: A, B ±10V Range: Impedance 2K/1M Ohm Nominal
- ±2.5V Range: Impedance 2K/1M Ohm Nominal Input Threshold Accuracy: 0.5% of Range
- High Impedane Inputs Impedance:
- Range: ±5V Zin > 4M Ohm Range: ±20V Zin = 2.1M Ohm
- Range: ±50V Zin = 2.1M Ohm Channel: 80(Use DS2803 as Relay Driver)

MATRIX BOARD

Contact Voltage: 60V

- Two-wire Force/Sense Input Channel
- Two-wire Force/Sense Output Channel Contact Current: 1A

Type Resistor: $<1\Omega$ AUTO-CALIBRATION &

· Standard Cells

1923 Hartog Drive

TEL: +1-408-567-9898

FAX: +1-408-567-9288

E-mail: triusa@tri.com.tw

No.207 Daewoo-Technopia,

TEL: +82-31-470-8858

E-mail: trikr@tri.com.tw

San Jose, CA 95131 U.S.A.

- PEB64 PATTERN BOARD
- Voltage Force and Measure Accuracy: ±(1% of range)

- Current Force and Measure Accuracy: ±(1% of range)
- Pin Level Source: Per Pin
- Driver Voltage Range: -2.0V to +8V Accuracy: ±(0.3% of 8V range)
- Comparator Voltage Range: -2.0V to +8V Accuracy: ±(0.5% of 8V range)
- Min. Pulse Width: 10ns
- Micro Instruction: CALL, RET, MATCH, MPAT.
- MLEN, RPT, LOOP, LPEND, STOP, TS
- Min. Period: 30ns 40ms
- Period Resolution: 10ns
- · Data Rate: 33MHz
- Timing Generators: 4 edge/Pin • Edge Placement Resolution: 2.5ns
- Timing Set: 16 change on the fly

- Pattern Memory Depth: 4M 8M Fail Memory Depth: 4K
- Time Measurement Resolution: 10ns

- DC Measurement: 16 bits
- AC Stimulus: 16 bits High Speed DAC
- Input Offset Voltage (Vos)
- Input Offset Current (los)
- Command Mode Rejection Ration (CMRR)
- Gain-Bandwidth Product (GBW)

- KVSBOARD
- 2 Channels Floating Ground System 16 bit ADC for voltage and current measure
- Vmax: 1000V

- Imax: 15mA Vrange: 100V, 200V, 500V, 1000V accuracy: 0.25% of range
- · Time Max: 10 sec

• TMU FOR TIMING PARAMETERS MEASUREMENT

- HIGH-PRECISION MEASUREMENT UNIT



5



- MULTIPLE V/I MODULES ARE SUITABLE FOR WIDE RANGE OF TEST APPLICATION
- ARBITRARY WAVEFORM GENERATOR AND DIGITIZER
- RICH DEBUGGING & ANALYTIC TOOLS

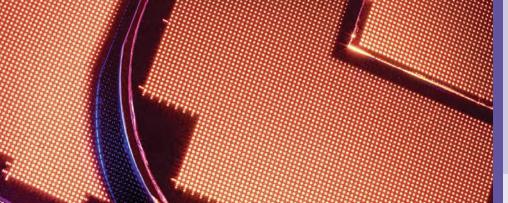
• EASY PROGRAMMING & DEBUGGING

• CAPABILITY TO GENERATE WAFER MAP & TRIM FILES





TRI



Test Research, Inc.

F., No.45, Dexing West Ra., Philip Dist Tainei City 11158, Tair

City Hsinchu County 30268 Taiw

DIAGNOSTIC BOARD

Standard DVM and GPIB Interface

- Pin Configuration (I/O Channels): 64 pins Parametric Measurement Unit: 8 Sets
- Specifications are subject to change without notice. All trademarks are the property of their owners.

TRI 德律 "Pi The absence of a product or service name or logo from this list does not constitute a waiver of TRI's trademark or other intellectual property rights concerning that name or logo. All other trademarks and trade names are the property of their owner

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Tokyo, 130-0021 Japan

TEL: +81-3-6273-0518

FAX: +81-3-6273-0519

E-mail: trijp@tri.com.tw

Shanghai, China

R

6

 Drive Mode: 0, 1, NF, NRZ, RZ, RO, SBC IO Mode: 0, 1, NF Strobe Mode: Edge/Window

Pattern Symbol: 0/1//X/Z/L/H

OP-LOOP BOARD

- Four OP AMP Loop
- Common Mode DAC: 16 bits
- AC measurement: 10MSPS, 12 Bit ADC • Loop Reference DAC: 16 bits
- Open Loop Gain (Avol)
- Power Supply Rejection Ration (PSRR)

Slew Rate (SR)

- 16 bit DAC for voltage and current force
- Irange: 15μA, 150μA, 1.5mA, 15mA I accuracy: 0.35% of range

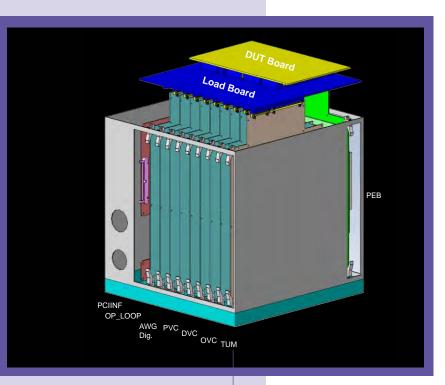
FEATURES TR6850S

OVC:

• 8 PMU with up to ±20V, ±200mA Forcing and Measuring Capability

• 16-bit resolution

THE BEST "POWER MANAGEMENT IC+ MIXED SIGNAL" TESTER



The TR6850S features a text mode program that links with Microsoft Visual Studio. NET 2005 for rich debugging and analytic tools. It runs on a Windows XP operating system and offers a userfriendly GUI.

TR6850S seamlessly connects to all popular probers and handlers, and supports max. 8-site parallel testing capability.

All these features make TR6850S the best "Power Management IC+Mixed Signal" tester for both engineering verification and mass production.

TR6850S Hardware Architecture

• TMU:

- 80 Relay-control and 4 TMUs
- Timing Parameters Testing for Pulse Width, Pulse Period Frequency, Propagation Delay, and Rising/Falling Time

PVC:

- 2 Floating Ground Pulse PMU with up to ±45V and 30A Forcing and Measuring Capability
- 16-bit Resolution

FEATURES:

- Max 64 pin logic I/O pin.
- High-precision measurement unit.
- 40MSPS/16bit Arbitrary waveform generator, 10MSPS/14 bit Digitizer.
- Max. 8-Sites parallel testing.
- Enable grading the test results.
- Easy Programming & Debugging: Link with Visual Studio.NET to develop a C++ test program.
- Rich Debugging & Analytic Tools: Module Debug Tools, SPC, Parametric Wafer Map Tool, Pattern Editor, Shmoo, etc.
- Capability to generate Wafer Map Files & Trim Files.
- Multiple Store Modes to meet different requirements in saving Datalog.
- · Yield Alarm Management.
- Able to analyze test performance.

TEST PROGRAM EDITOR

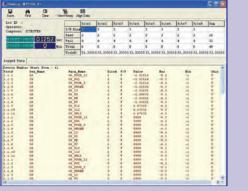
TR6850S links with Visual Studio.NET to provide an easy programming, easy debugging environment. TR6850S S/W system will auto-create a new function skeleton for each new inserted sequence item. Users only need to focus on developing test logic.

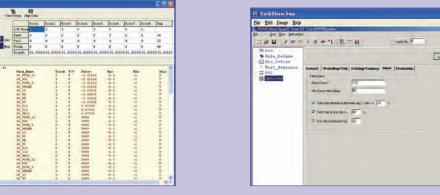
Users can easily add 3rd party's library to enhance test performance or control external instruments.

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DATALOG WINDOW

Display readable aligned test data, 1 line per test parameter. If the test result is fail, the output will be highlighted in red.





YIELD ALARM

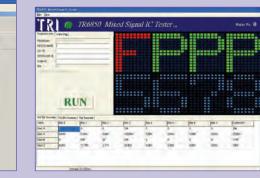
The TR6850S will issue a warning message & turn prober buzzer ON when yield is abnormal. The yield management can warn the operator to check what's wrong early and improve the mass production yield. There are 3 abnormal yield types that can be checked out:

- 1. Any two sites' yield difference is greater than a set criterion.
- 2. Any site's yield is too low.
- 3. Any site's continuous fail count exceeds a specified count.



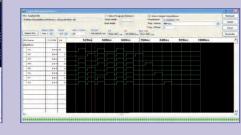
PRODUCTION WINDOW

Compact window to display test results & summary in mass production.



AC RESULT ANALYSIS TOOLS

The timing diagram help user understand relative timing among tester input and device output. With the graphic waveform, user can verify edited waveform as well.



-148957.80 0.157769 X Range -100001 80000 Unit USL LSL Max. Min. Mean ShiDev Cp Cpt Ch Value 8.2 d 0.157769 0.000000 0.832580 0.024413 1.365428 0.329572 if sind 8

TEST SEQUENCE

Table to designate compared

parametrics and test flow.

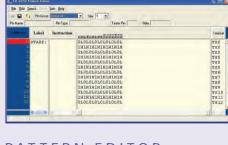
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EDITOR

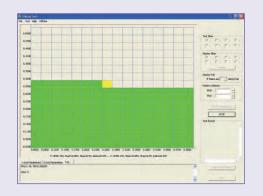
SPC TOOL

Engineering analysis tool to view each test parameter's statistics: test value histogram, min/max/mean value, standard deviation, Cp, Cpk.



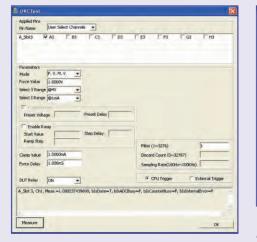
PATTERN EDITOR

View pattern file offline or view pattern memory online. Enable modifing pattern and save to file, also refreshing the patterns online by designating a specified range.



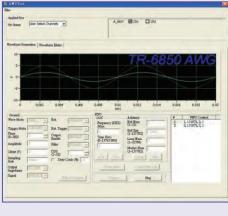
SHMOO TOOL

Enable to vary multiple parameters simultaneously to scan device's boundary.



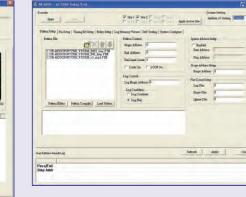
DC DEBUG TOOLS

The TR6850S provides several DC modules (eg. OVC, PVC, OPAMP-LOOP) to measure DC parameters in different purposes. Users can change the testing parameters to process various measurements in the tool.



AWG/DIGITIZER DEBUG TOOLS

Use AWG & Digitizer debug tools to control the TR6850S AWG/Digitizer module for debugging purpose.



AC DEBUG TOOLS

The TR6850S provides two AC modules (eg. PEB64, TMU) to measure AC parameters in different purposes. Users can change the testing parameters to process various measurements in the tool.